**600085 Embedded Systems Development**

**Group: 5**

**Demonstration Feedback:**

Overall good implementation. Nice screen design with a full set of parameters showing on the main screen. Friendly settings design. Not able to mute sound. Could have implemented coarse and find tuning. Output controls are indicated in LCD but no I/O implemented, no persistanct configurations. Limited code comments, well design architecture.

Mark awarded: 73/100.

**Group Report Assessment:**

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| Assessment Item | Comments | Weight |
| System Deliverables should at least cover these points: (A1,A2)   * requirements and specifications * overall system design * interfaces and pin mappings * listing of drivers * functions and features * control flow * operating manual | Nice front page. Good overall system introduction to the project. The requirements could be more details and can be tested.  The pin mappings are well documented. The Figure 1 needs some tidy up.  Good diagrams in section 3, it would have been helpful to have some explatory texts to describe them.  Nice and easy to follow operating manual. | 50%  33 |
| Software Design: (A3)   * Appropriate device driver design * Code documention and comments | The drivers listed are useful but they are more like user level functions, the interfaces interacting with the components should be listed as drivers, e.g. a function allows the new values to be written to the clock. | 20%  13 |
| Implementation and Test: (A3)   * Testing performed | The testing has been missed out in the report. | 20%  4 |
| Critical Evaluation and Group effectiveness: (A2)   * Conclusions drawn * Lessons learnt * Role of each group member in the development. | Clear role divisions though the tasks could be better distributed.  The reflection could be more specific with details in the system level matters. | 10%  7 |

**Overall Comments**

\_\_\_\_\_\_ The presentation of the report lacks of some final touches, testing results should have been added.\_\_\_\_\_\_\_

Mark Awarded: \_\_\_57\_\_\_\_\_\_\_\_\_\_